SHEET 1 OF 24

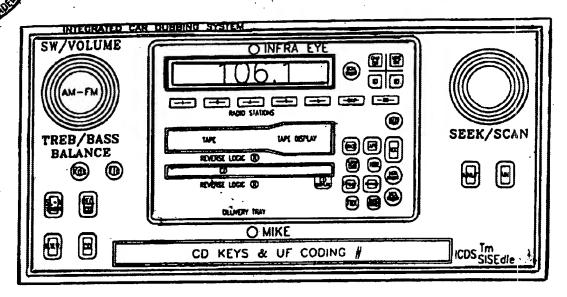


Fig.1

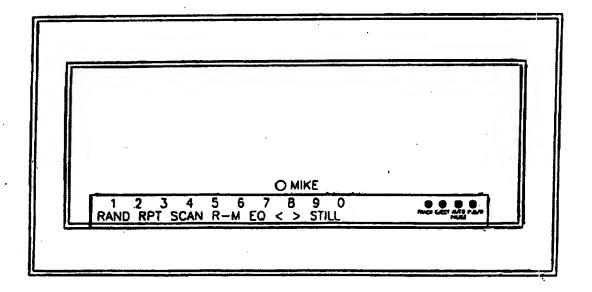


Fig.2



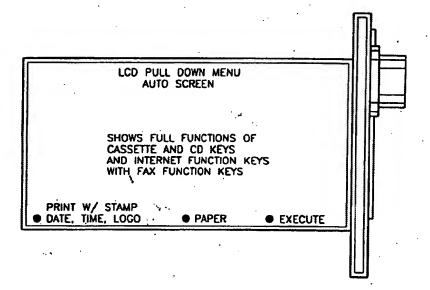


Fig.3

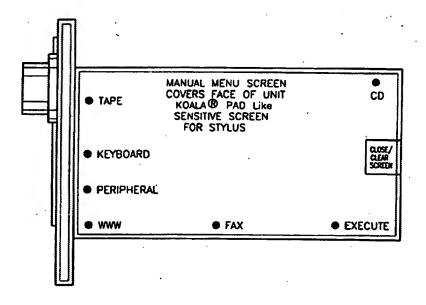


Fig.4



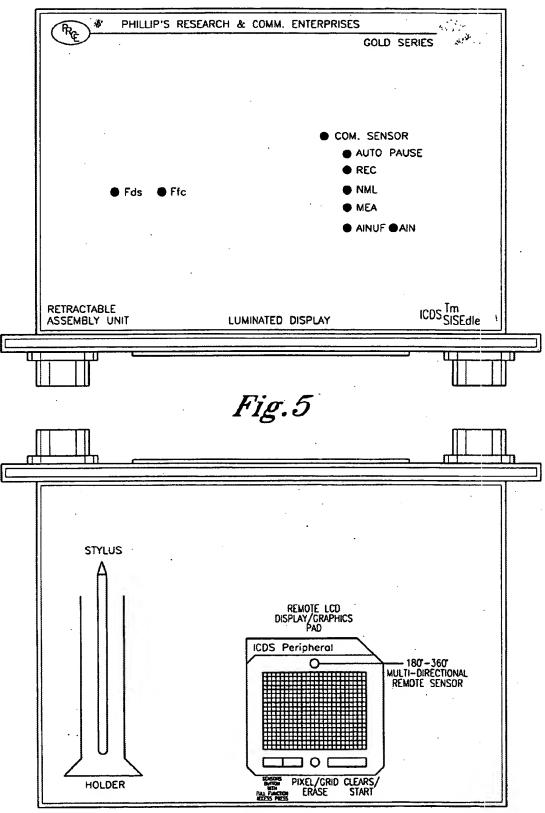
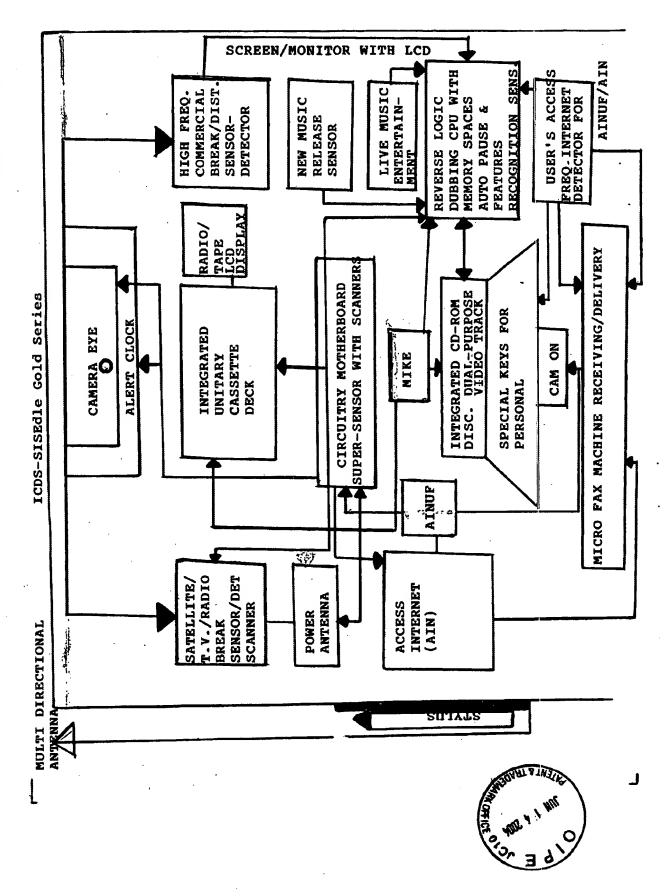
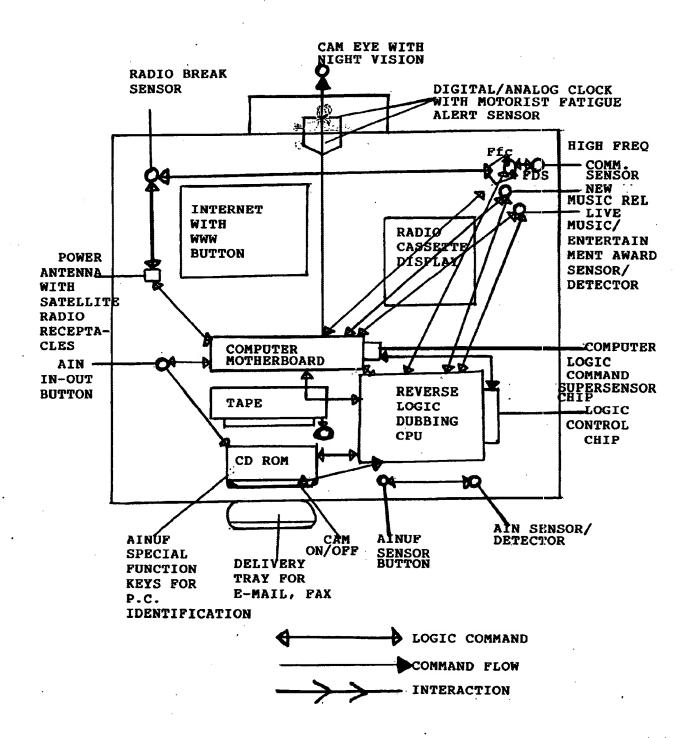


Fig.6



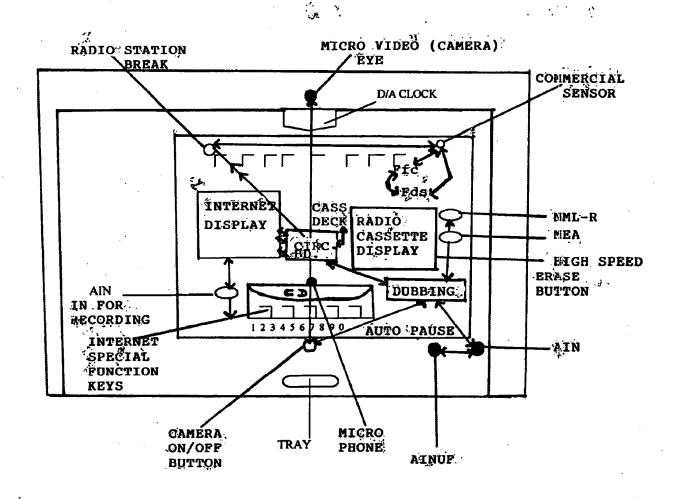


## BLUE PRINT



1





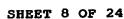


FIG. 11

ICDS-SISE GOLD SERIES - Top of the Model

CSISX MODEL

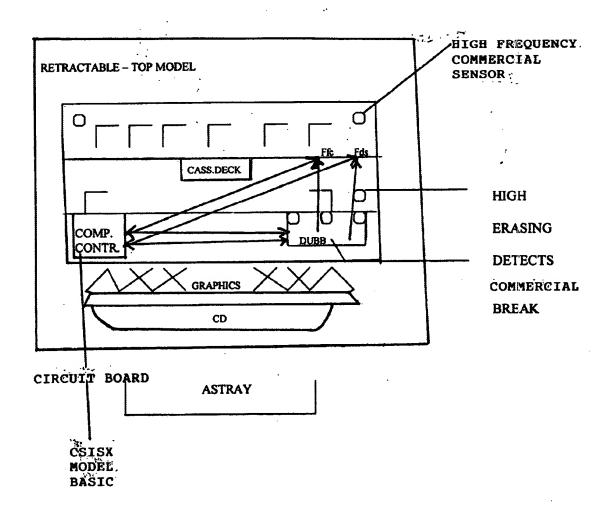
- Commercial Sensor deactivated

SIS MODEL

- Commercial Super Sensor Chip

SISE MODEL

- Erasing Key (deluxe)



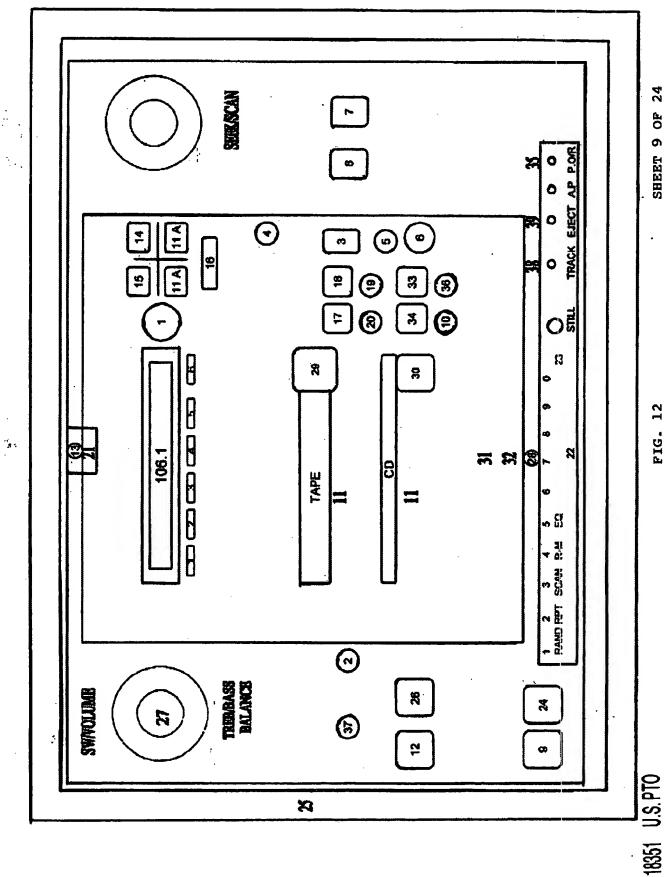
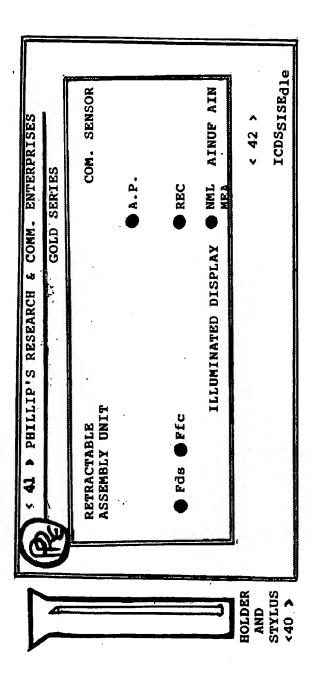
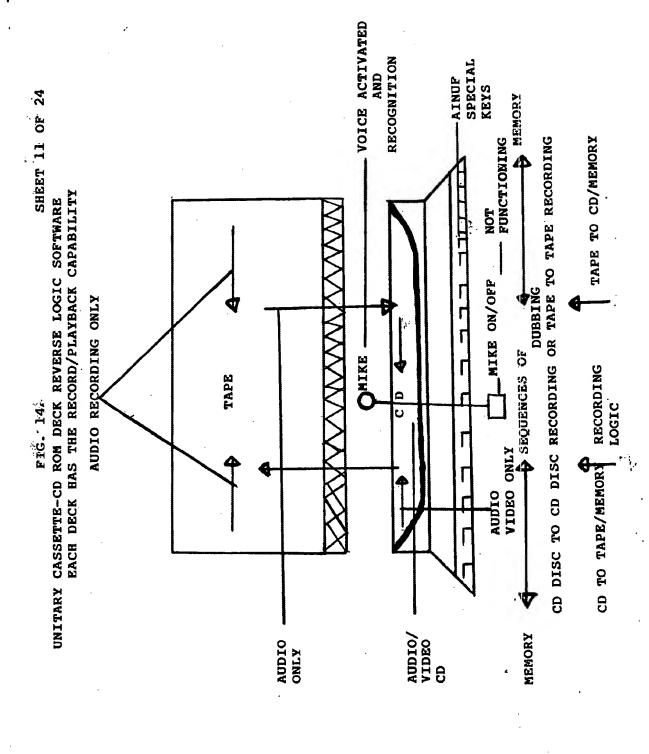


FIG. 12





7



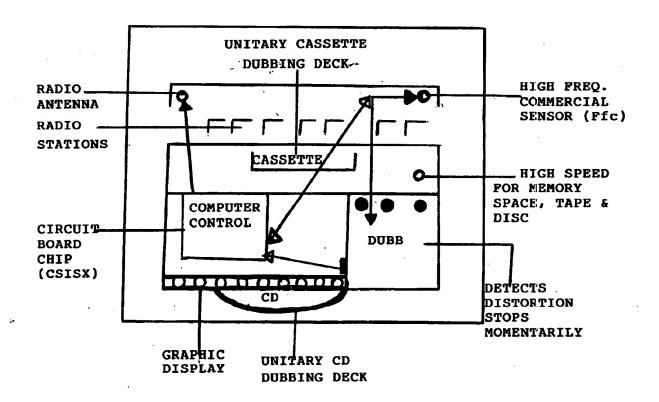


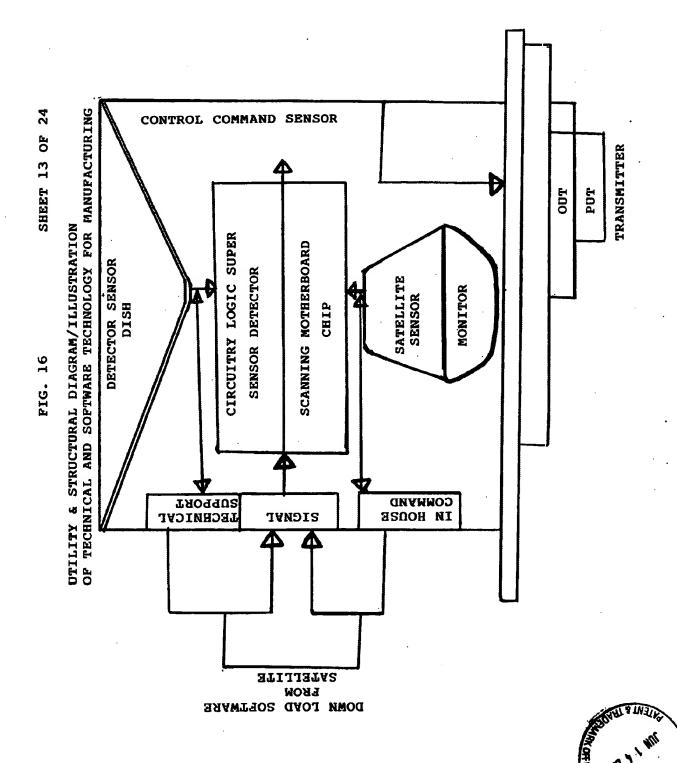
:1



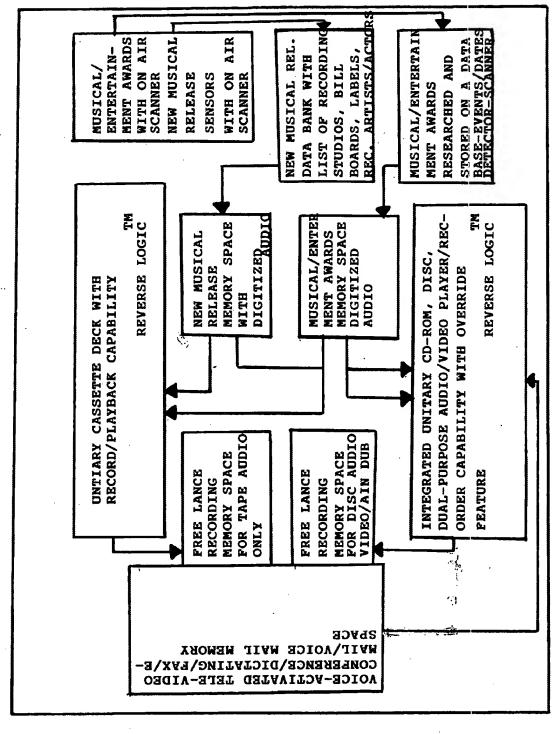


# COMPUTER CONTROL COMMAND WITH COMMERCIAL FREE-DISTORTION FREE DUBBING





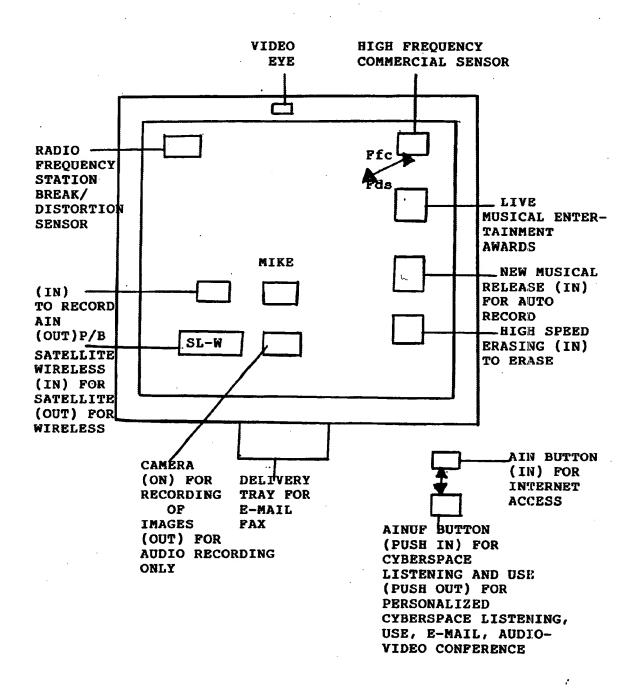
LOGIC MEMORY SPACES- INTERNAL CONFIGURATION AND LOGIC FLOW

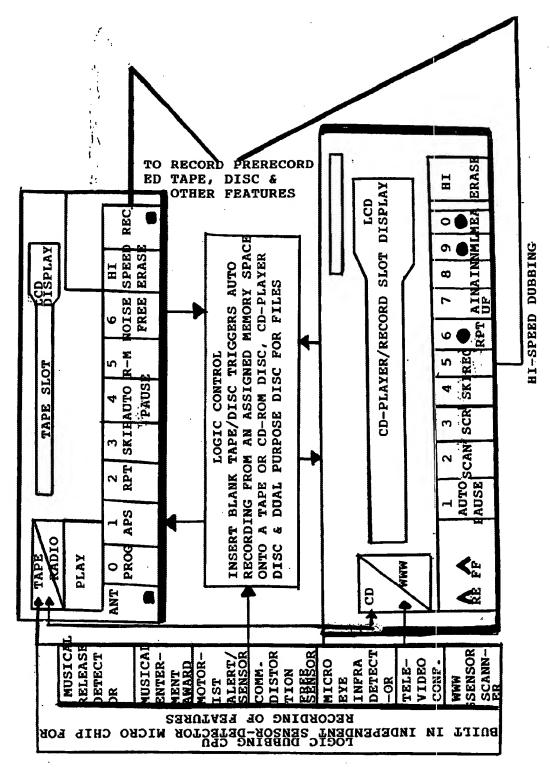




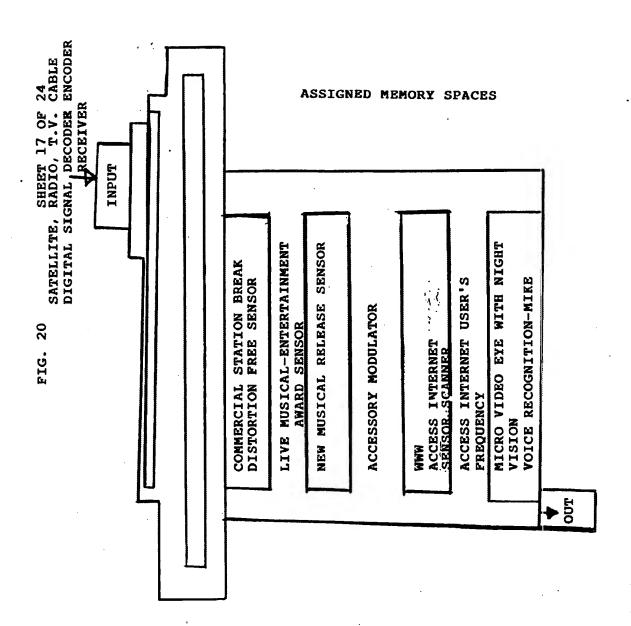


### USE OF FUNCTIONAL PARTS/COMPONENTS

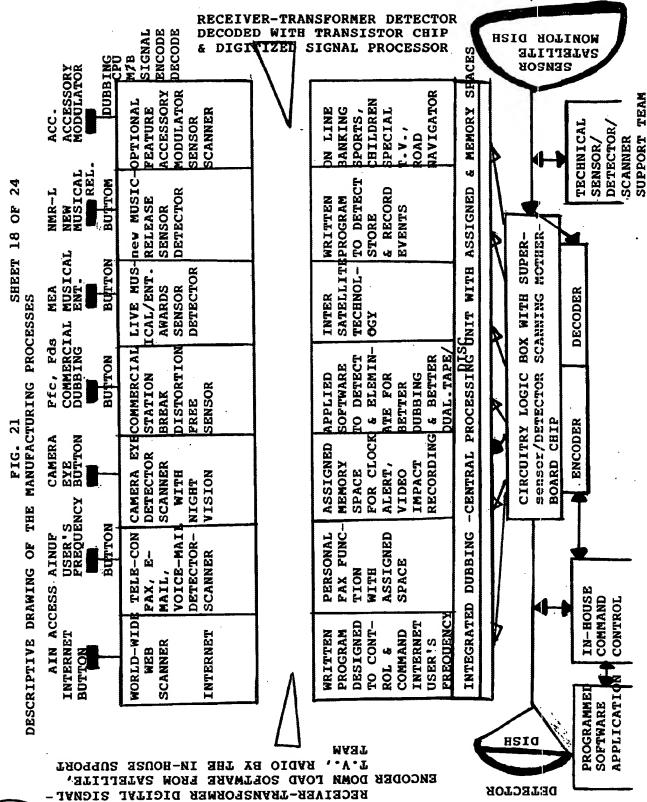






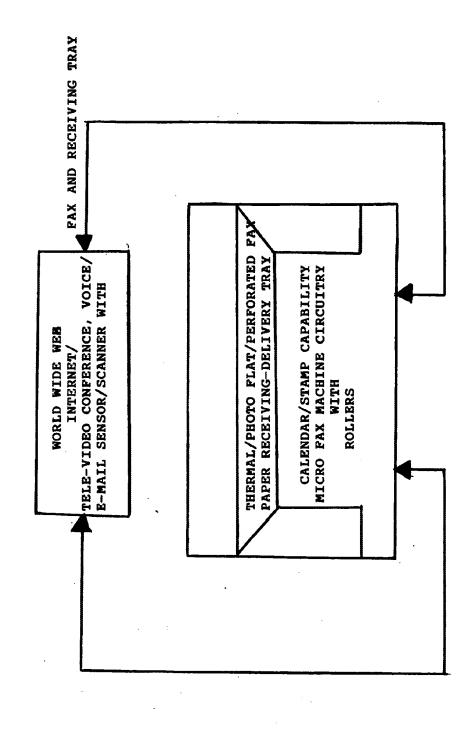






OLD B OLD BANKER THANKS

INTERNET-PAX FUNCTIONS







### REVERSE LOGIC WITH MEMORY SPACES

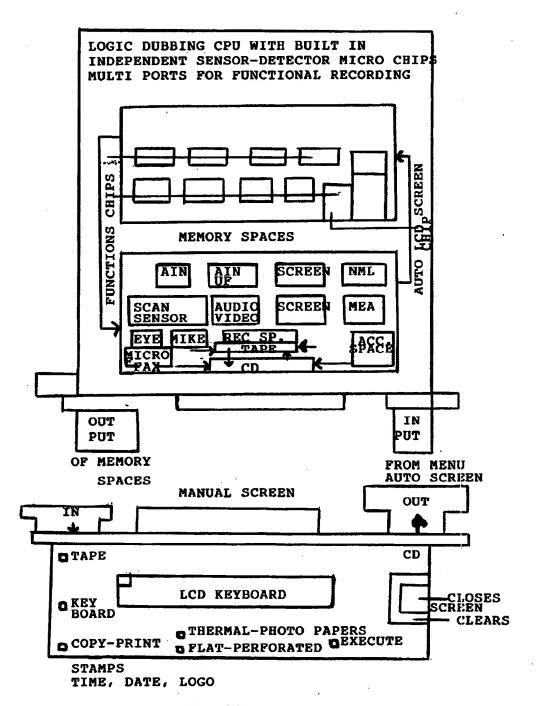
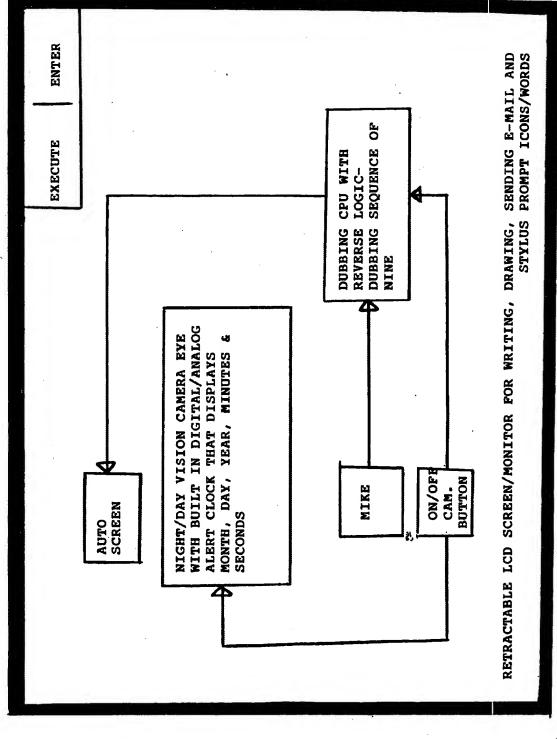


FIG. 24







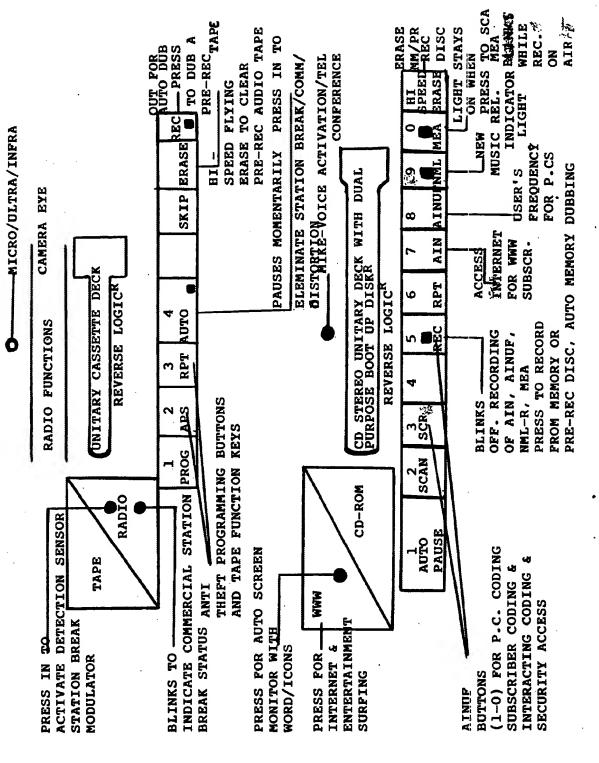




FIG. 27

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# RETRACTABLE ASSEMBLY HOUSING

